

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,845,472 B2
APPLICATION NO. : 09/769956
DATED : January 18, 2005
INVENTOR(S) : William J. Walker et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 3, line 32, delete “Pentium HI Xeon” and insert -- Pentium II Xeon --, therefor.

In column 4, line 11, after “interface” delete “121.” and insert -- 21. --, therefor.

In column 6, line 7, after “cartridge” delete “44a” and insert -- 42a --, therefor.

In column 6, line 20, after “cartridge” delete “44a” and insert -- 42a --, therefor.

In column 11, line 4, delete “ran” and insert -- run --, therefor.

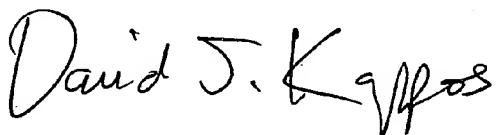
In column 14, line 30, in Claim 45, after “comprises” insert -- a --.

In column 16, line 16, in Claim 73, delete “REAR” and insert -- READ --, therefor.

In column 16, lines 48-49, in Claim 78, delete “to a system” and insert -- to a memory sub-system --, therefor.

Signed and Sealed this

Twenty-second Day of December, 2009



David J. Kappos
Director of the United States Patent and Trademark Office